

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10002925	11/02/2001	257		2813	BEREZNY, N

**APPLICANTS: Kux Andreas; Smola Michael;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF PCT/EP00/03834 04/27/2000

** FOREIGN APPLICATIONS VERIFIED:

EUROPEAN PATENT OFFICE (EPO) 99 108 768.5 05/03/1999

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	1999P1778
Verified and Acknowledged Examiners's initials		
TITLE : Method and device for securing a multi-dimensionally constructed chip stack and chip configuration		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		Assistant Examiner	
		Primary Examiner	
		PREPARED FOR ISSUE	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.
TERMINAL		Print Fig.	
DISCLAMER		Application Examiner	
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